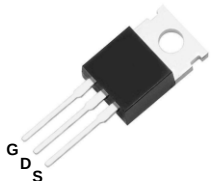
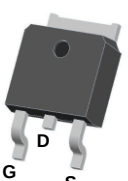
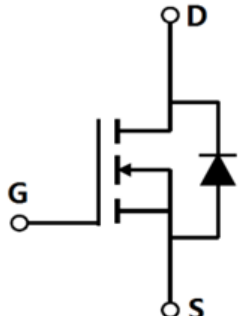


N-channel 60V, 10mΩ max.,
SGT MOSFET S2 in TO-220 and TO-252

Datasheet - production data

1. Descriptions

TO-220	TO-252
	
Schematic Diagram 	

Key Performance Parameters

Parameters	Value	Unit
BV_{DSS}	60	V
$R_{DS(on),max}$	10	mΩ
$Q_{g,typ}$	15.8	nC
$I_{D,pulse}$	160	A
E_{AS}	100	mJ

Features

- Extremely low losses due to very low FOM $R_{dson} \cdot Q_g$.
- High-speed switching.
- Qualified for industrial grade applications according to JEDEC.
- 100% UIS Tested.

Applications

High-Efficiency DC-DC Converters, Switching Voltage Regulators and Motor Drivers.

Type/Ordering Code	Package	Marking	Related Links
CSP100N06S2	TO-220	100N06S2	see Appendix A
CSD100N06S2	TO-252		

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2. Maximum Ratings

at $T_j = 25^\circ\text{C}$, unless otherwise specified

Table 1. Absolute Maximum Ratings

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
V_{DS}	Drain-source voltage ¹⁾	-	-	60	V	$V_{GS}=0V$, $I_D=250\mu A$
I_D	Continuous drain current ²⁾	-	-	60	A	$T_C=25^\circ\text{C}$
$I_{D,pulse}$	Pulsed drain current	-	-	160	A	$T_C=25^\circ\text{C}$
E_{AS}	Avalanche energy, single pulse ³⁾	-	-	100	mJ	$I_D=20A$; $V_{DD}=50V$
I_{AS}	Avalanche current	-	-	20	A	-
V_{GS}	Gate source voltage	-20	-	20	V	static; AC ($f>1\text{ Hz}$)
P_{tot}	Power dissipation	-	-	82	W	$T_C=25^\circ\text{C}$
T_j, T_{stg}	Operating and storage temperature	-55	-	175	$^\circ\text{C}$	-
I_S	Continuous diode forward current	-	-	60	A	$T_C=25^\circ\text{C}$
$I_{S,pulse}$	Diode pulse current ²⁾	-	-	160	A	$T_C=25^\circ\text{C}$

1) Limited by T_j max. Maximum duty cycle $D=0.75$.

2) Pulse width t_p limited by $T_{j,max}$.

3) $V_{DD}=50V$, $L=0.5mH$, $R_G=25\Omega$, Starting $T_j=25^\circ\text{C}$.

3. Thermal Characteristics

Table 2. Thermal Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
R_{thJC}	Thermal resistance, junction - case	-	-	1.52	°C/W	$T_C = 25^{\circ}\text{C}$
R_{thJA}	Thermal resistance, junction - ambient	-	-	60	°C/W	$T_C = 25^{\circ}\text{C}$
T_{sold}	Soldering temperature, wavesoldering only allowed at leads	-	-	260	°C	Lead Temperature (Soldering, 10 sec)

4. Electrical Characteristics

at $T_j = 25^\circ\text{C}$, unless otherwise specified

Table 4. Static Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
$V_{(BR)DSS}$	Drain-source breakdown voltage	60	-	-	V	$V_{GS}=0V, I_D=250\mu A$
$V_{(GS)th}$	Gate threshold voltage	1	1.8	2.5	V	$V_{DS}=V_{GS}, I_D=250\mu A$
I_{DSS}	Zero gate voltage drain current	-	-	1	μA	$V_{DS}=60V, V_{GS}=0V, T_j=25^\circ C$
I_{GSS}	Gate-source leakage current	-	-	± 100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
$R_{DS(on)}$	Drain-source on-state resistance	-	9.5	10	m Ω	$V_{GS}=10V, I_D=20A, T_j=25^\circ C$
R_G	Gate resistance	-	2.6	-	Ω	$V_{DD}=0V, V_{GS}=0V, F=1MHz$
g_{fs}	Transconductance		50		S	$V_{DS}=5V, I_D=20A$

Table 5. Dynamic Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
C_{iss}	Input capacitance	-	1070	-	pF	$V_{GS}=0V, V_{DS}=30V, f=250kHz$
C_{oss}	Output capacitance	-	192	-	pF	$V_{GS}=0V, V_{DS}=30V, f=250kHz$
C_{rss}	Reverse transfer capacitance	-	8	-	pF	$V_{GS}=0V, V_{DS}=30V, f=250kHz$
$t_{d(on)}$	Turn-on delay time	-	7	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=36A, R_G=2.7\Omega$
t_r	Rise time	-	58	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=36A, R_G=2.7\Omega$
$t_{d(off)}$	Turn-off delay time	-	14	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=36A, R_G=2.7\Omega$
t_f	Fall time	-	84	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=36A, R_G=2.7\Omega$

Table 6. Gate Charge Characteristics

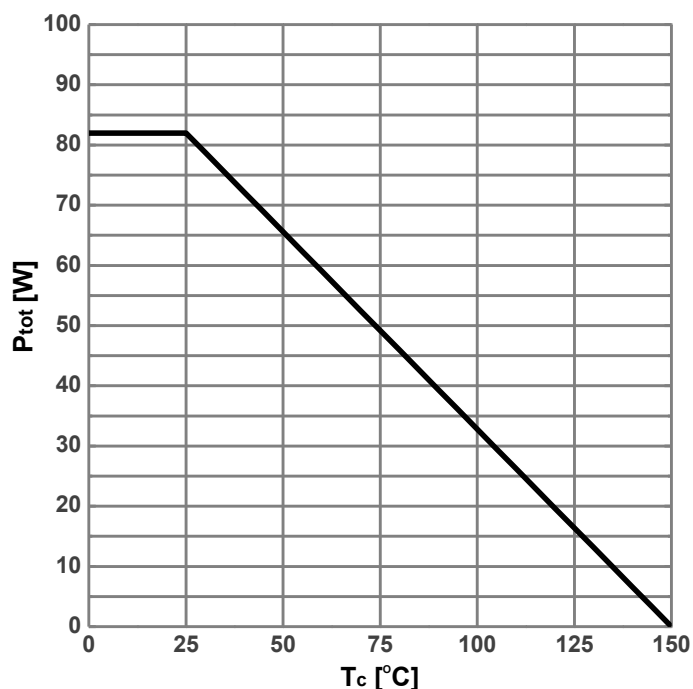
Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
Q_{gs}	Gate to source charge	-	2.43	-	nC	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$
Q_{gd}	Gate to drain charge	-	1.68	-	nC	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$
Q_g	Gate charge total	-	15.8	-	nC	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$
$V_{plateau}$	Gate plateau voltage	-	2.35	-	V	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$

Table 7. Reverse Diode Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
V_{SD}	Diode forward voltage	-	0.87	-	V	$V_{GS}=0V, I_F=20A, T_j=25^\circ C$
t_{rr}	Reverse recovery time	-	15	-	ns	$V_R=30V, I_F=36A, di_F/dt=100A/\mu s$
Q_{rr}	Reverse recovery charge	-	6	-	nC	$V_R=30V, I_F=36A, di_F/dt=100A/\mu s$
I_{rrm}	Peak reverse recovery current	-	0.7	-	A	$V_R=30V, I_F=36A, di_F/dt=100A/\mu s$

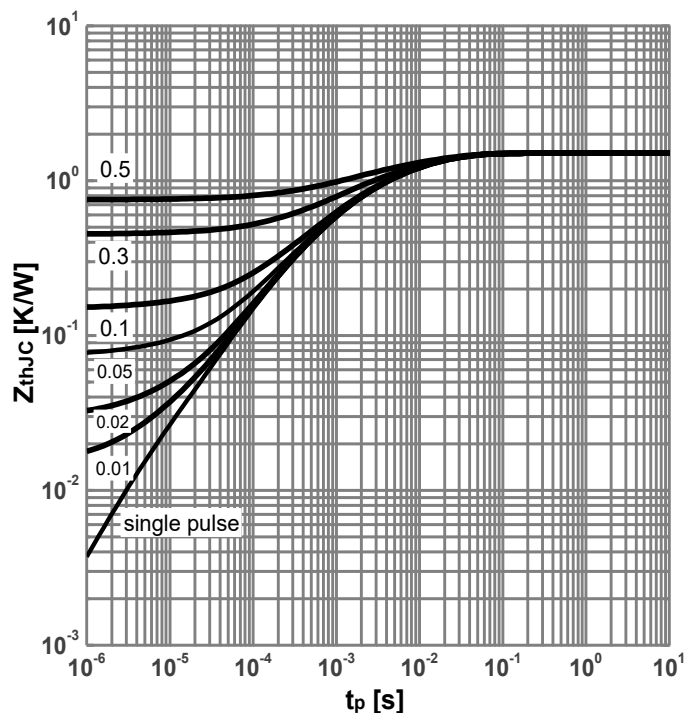
5. Electrical Characteristics Diagrams

Diagram 1: Power dissipation



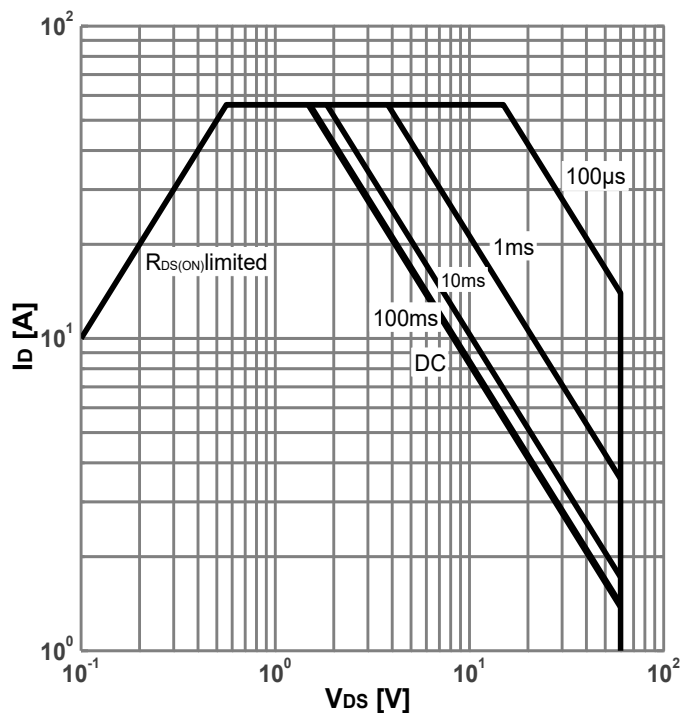
$$P_{tot}=f(T_c)$$

Diagram 2: Max. transient thermal impedance



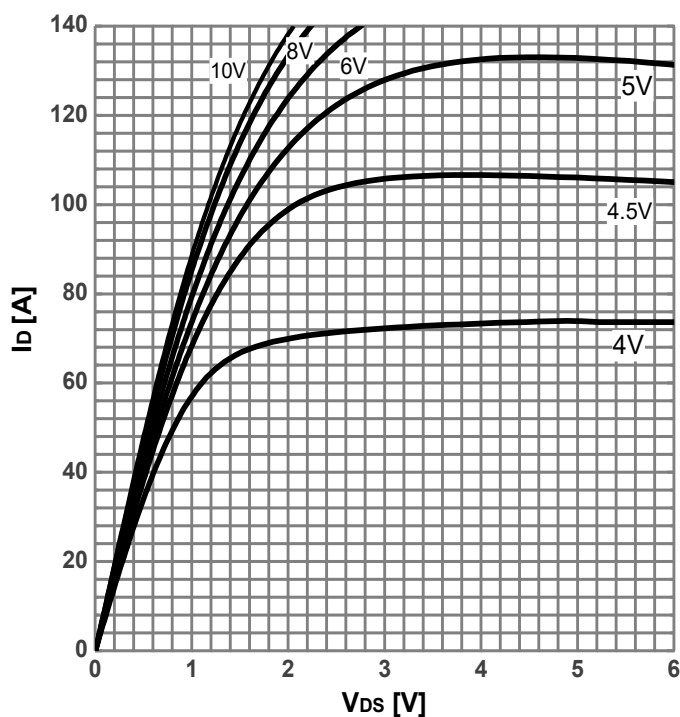
$$Z_{thJC}=f(t_p); \text{ parameter: } D= t_p/T$$

Diagram 3: Safe operating area



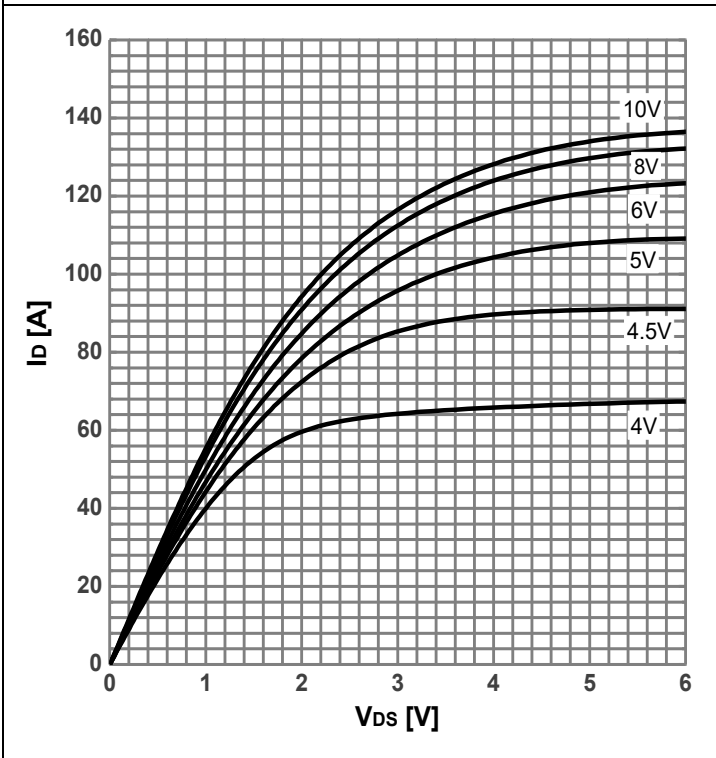
$$I_D=f(V_{DS}); T_J=25^{\circ}\text{C}; D=0; \text{ parameter: } t_p$$

Diagram 4: Typ. output characteristics



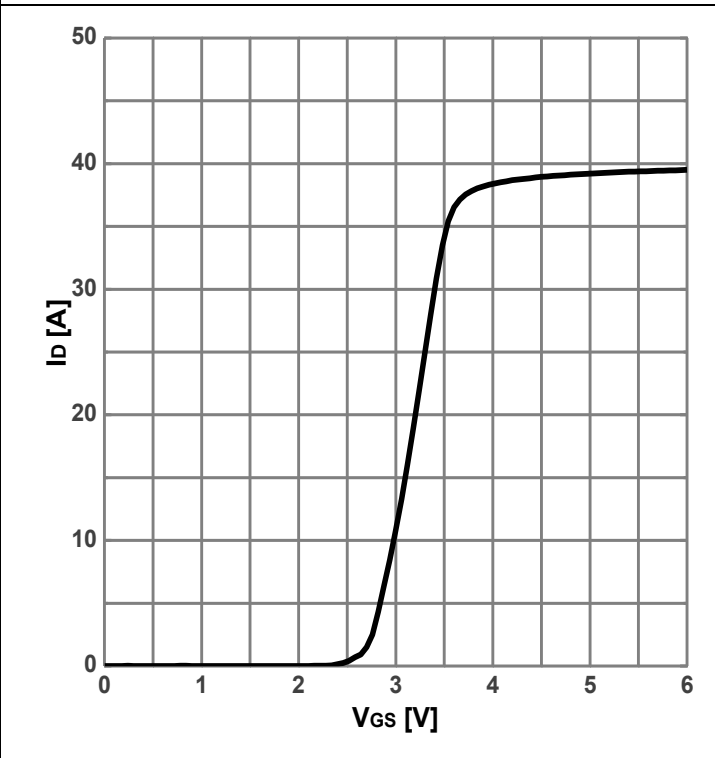
$$I_D=f(V_{DS}); T_J=25^{\circ}\text{C}; \text{ parameter: } V_{GS}$$

Diagram 5: Typ. output characteristics



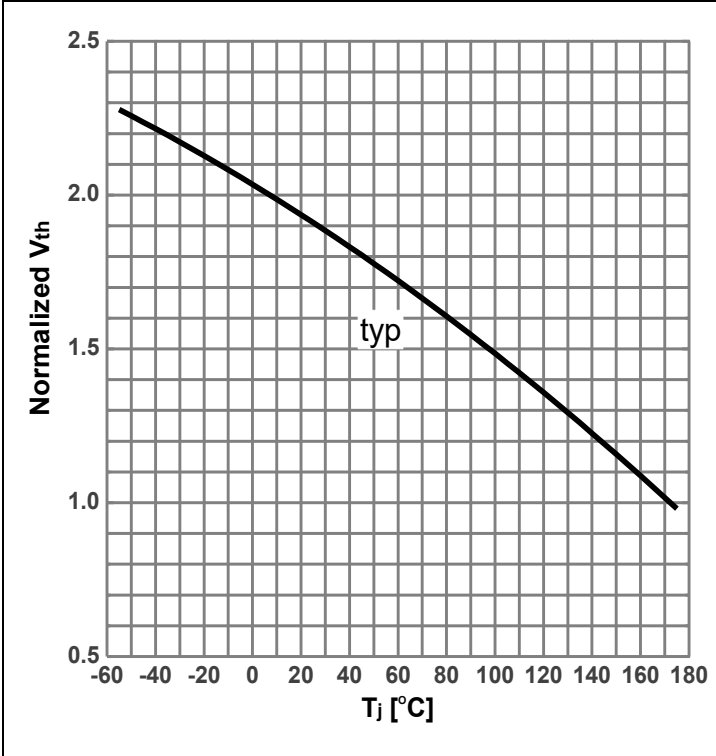
$I_D=f(V_{DS})$; $T_J=125^{\circ}\text{C}$; parameter: V_{GS}

Diagram 6: Typ. transfer characteristics



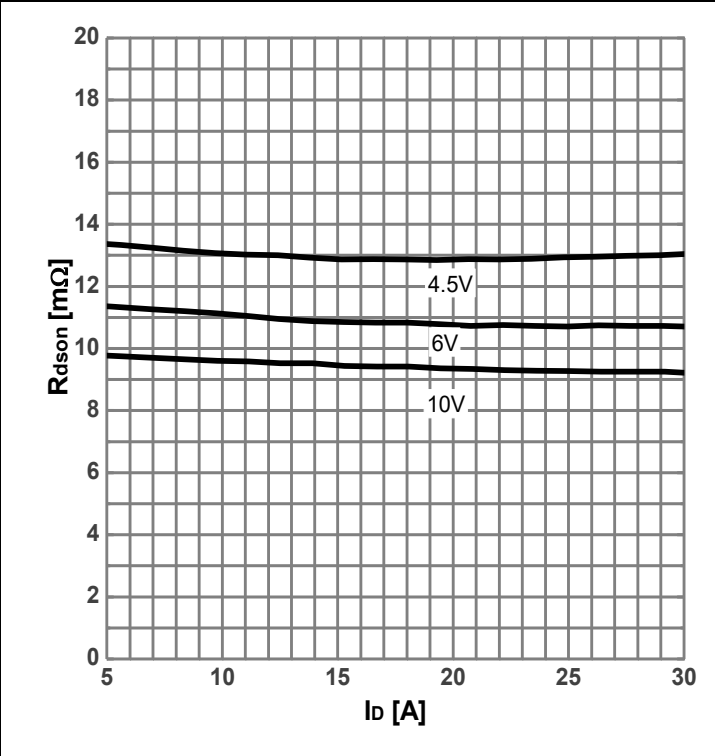
$I_D=f(V_{GS})$; $V_{DS}=5\text{V}$; $T_J=25^{\circ}\text{C}$

Diagram 7: Gate threshold voltage vs. Junction temperature



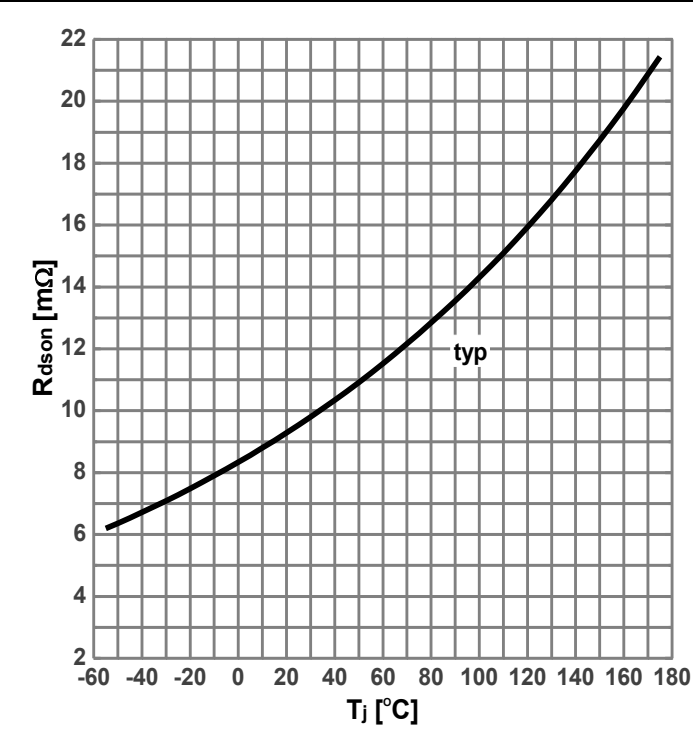
$V_{th}=f(T_J)$; $I_D=250\mu\text{A}$

Diagram 8: On-state resistance vs. Drain current



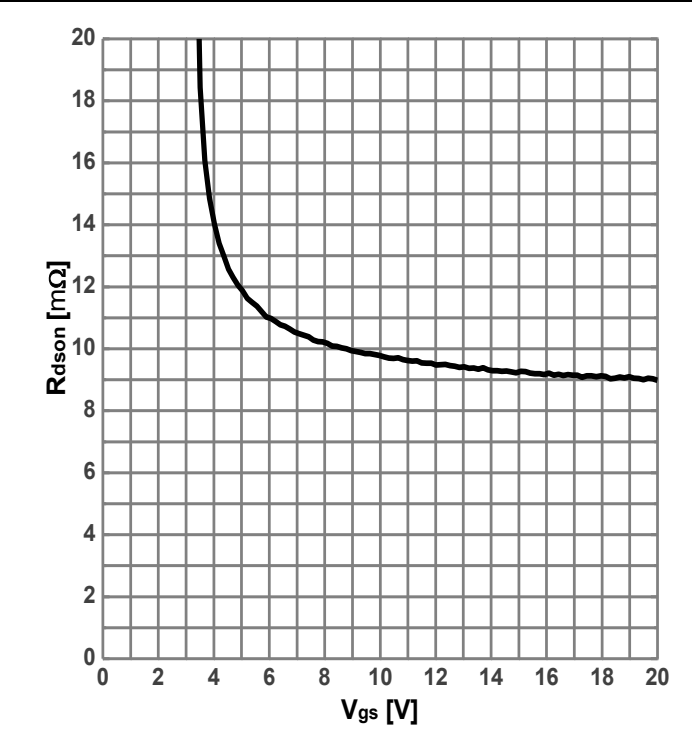
$R_{DS(on)}=f(I_D)$; $T_J=25^{\circ}\text{C}$; parameter: V_{GS}

Diagram 9: On-state resistance vs. Junction temperature



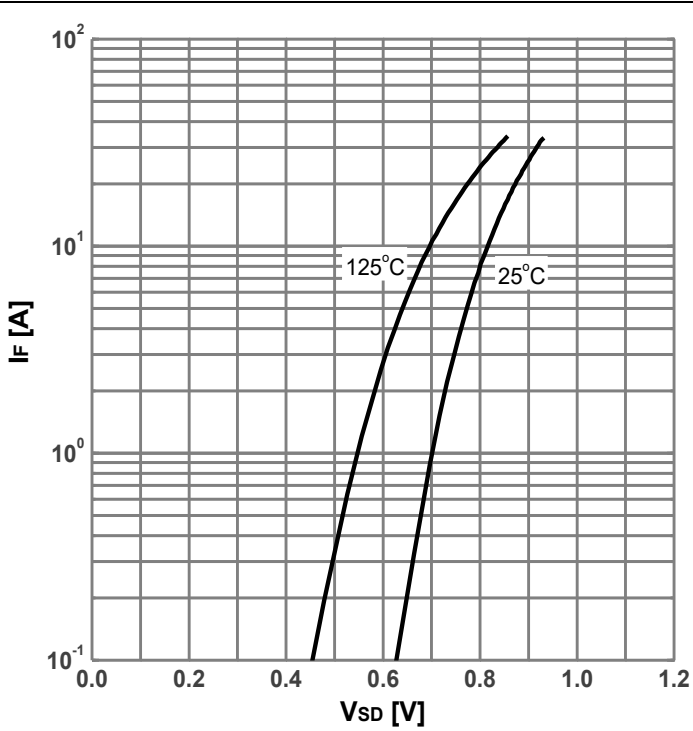
$R_{DS(on)}=f(T_j)$; $I_D=20A$; $V_{GS}=10V$

Diagram 10: On-state resistance vs. Vgs characteristics



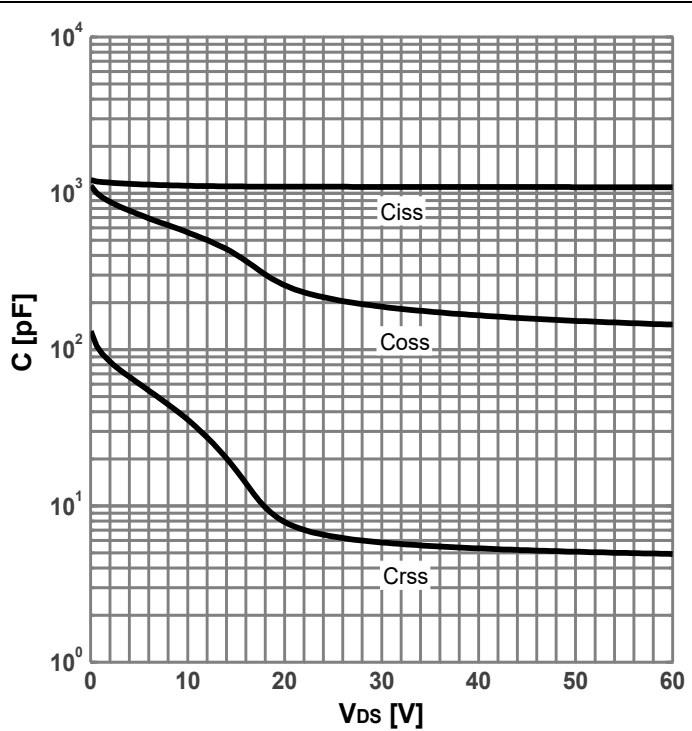
$R_{DS(on)}=f(V_{gs})$; $T_j=25^{\circ}C$; $I_D=20A$

Diagram 11: Forward characteristics of reverse diode

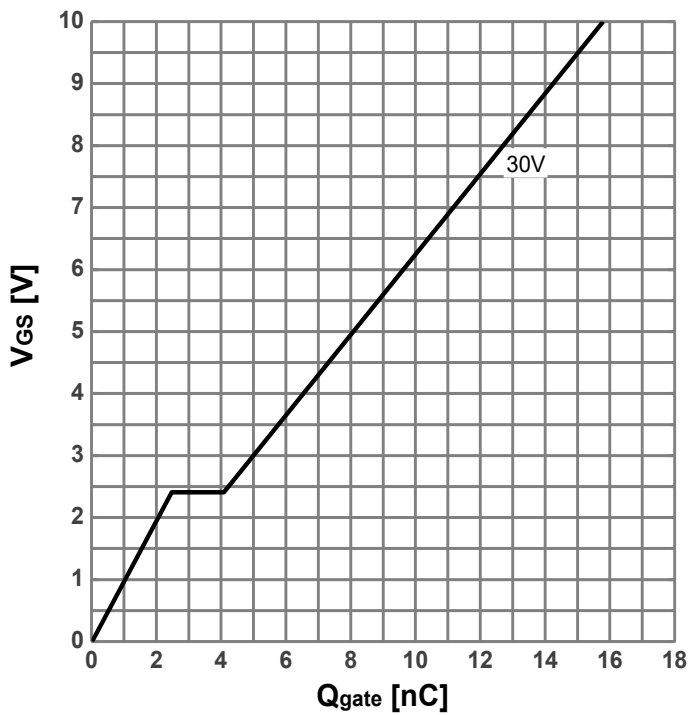


$I_F=f(V_{SD})$; parameter: T_j

Diagram 12: Typ. capacitances



$C=f(V_{DS})$; $V_{GS}=0V$; $f=1MHz$

Diagram 13: Typ. gate charge

$V_{GS}=f(Q_{gate})$; $I_D=20A$ pulsed; $V_{DS}=30V$

6. Test Circuits

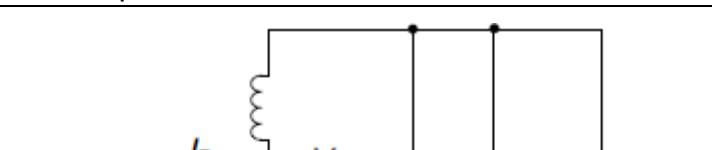
Table 7. Diode Characteristics

Test circuit for diode characteristics $R_{g1} = R_{g2}$	Diode recovery waveform $V_{DS(peak)}$, V_{DS}, I_F, I_{mn}, dI_F/dt, t_F, t_T, t_S, Q_F, Q_S, $Q_{Tr} = Q_F + Q_S$ at $10\% I_{mn}$
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Table 8. Switching Times

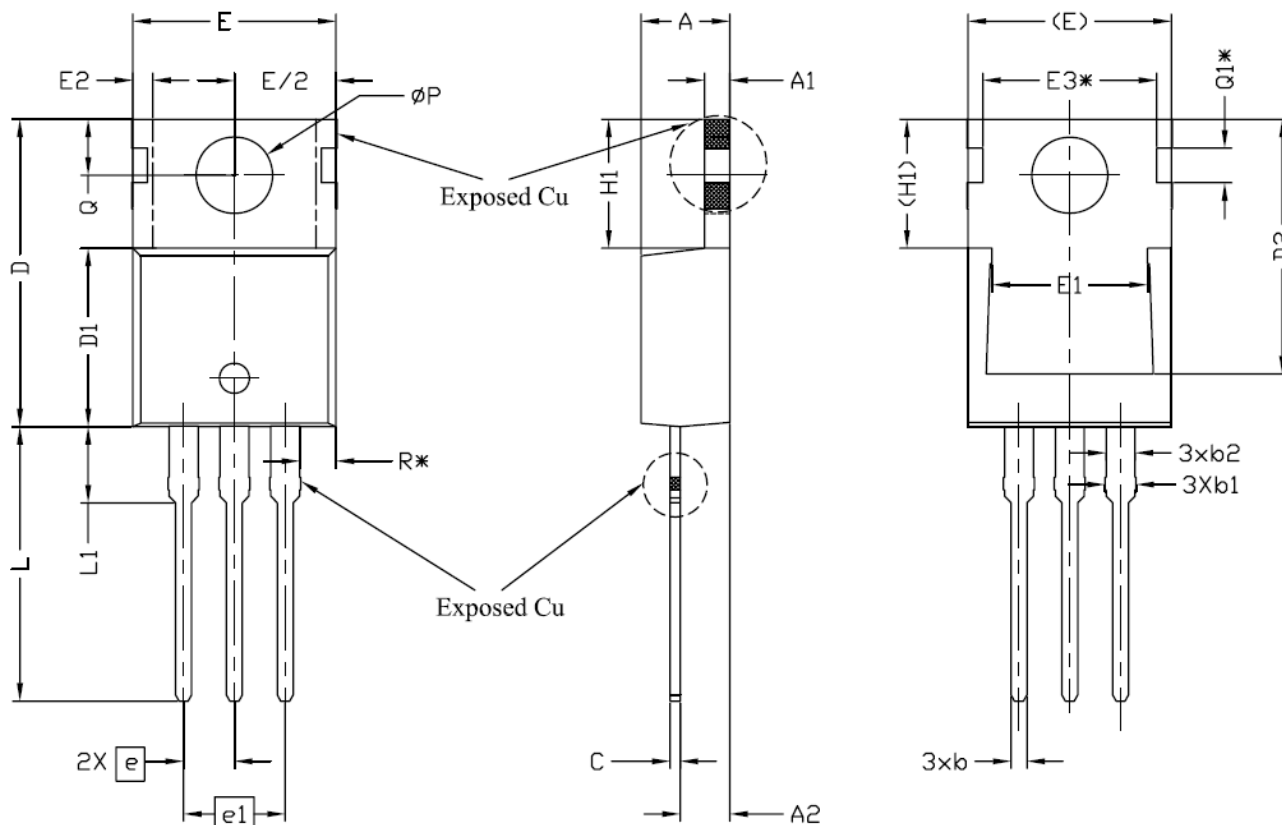
Switching times test circuit for inductive load 	Switching times waveform 
---	---

Table 9. Unclamped Inductive Load

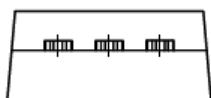
Unclamped inductive load test circuit 	Unclamped inductive waveform 
---	--

7. Package Outlines

Figure 1 Outline TO-220 Dimensions in mm



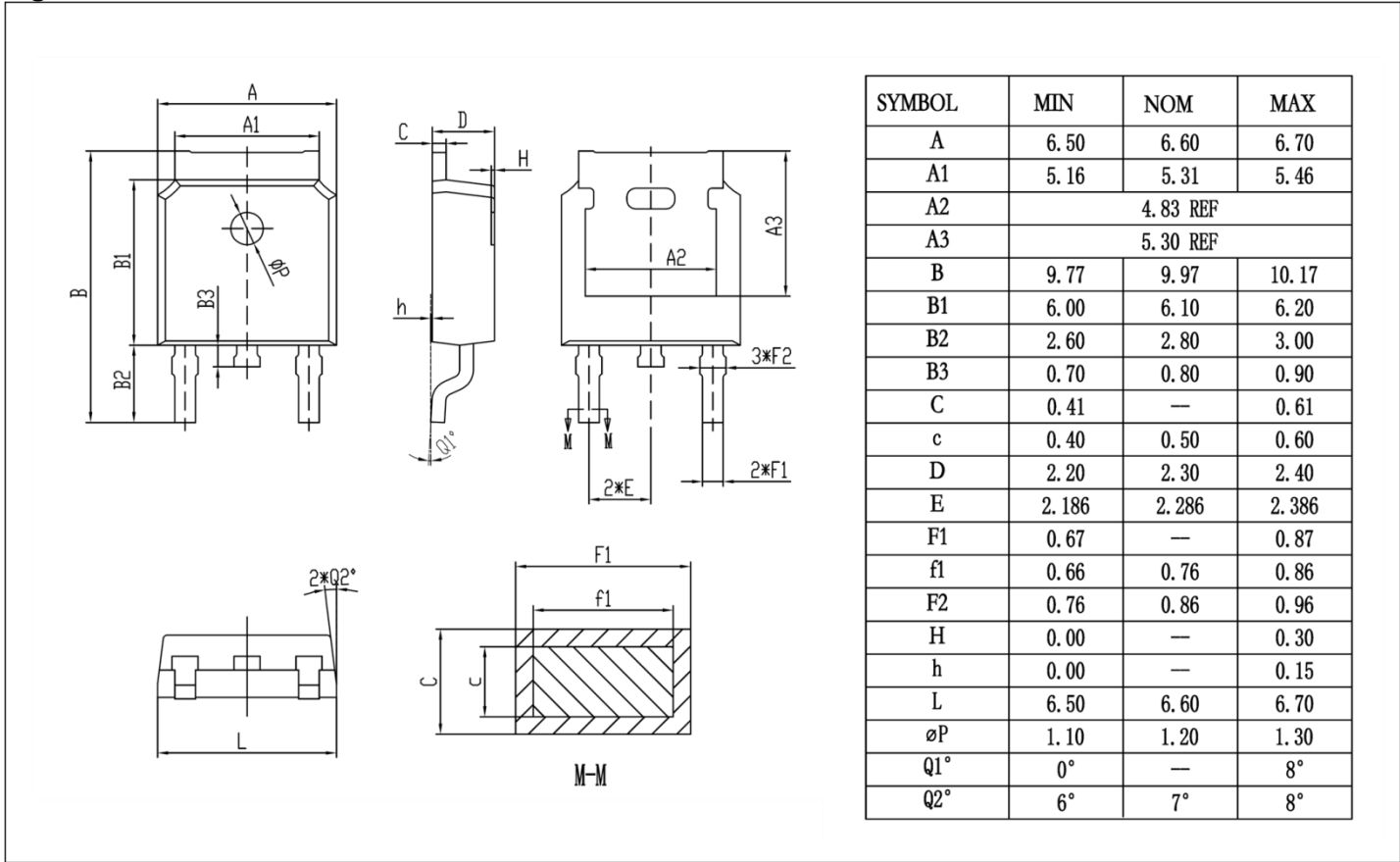
SYMBOL	DIMENSIONS			NOTES
	MIN.	NOM.	MAX.	
A	4.24	4.44	4.64	
A1	1.15	1.27	1.40	
A2	2.30	2.48	2.70	
b	0.70	0.80	0.90	
b1	1.20	1.55	1.75	
b2	1.20	1.45	1.70	
c	0.40	0.50	0.60	
D	14.70	15.37	16.00	4
D1	8.82	8.92	9.02	
D2	12.43	12.73	12.83	5
E	9.96	10.16	10.36	4,5
E1	6.86	7.77	8.89	5
E2	-	-	0.76	6
E3*	8.70REF.			
e	2.54BSC			
e1	5.08BSC			
H1	6.30	6.45	6.60	5,6
L	13.47	13.72	13.97	
L1	3.60	3.80	4.00	
ØP	3.75	3.84	3.93	
Q	2.60	2.80	3.00	
Q1*	1.73REF.			
R*	1.82REF.			



Note:

1. Package Reference: JEDEC TO220, Variation AB.
2. All Dimensions Are In mm.
3. Slot Required, Notch May Be Rounded
4. Dimension D & E Do Not Include Mold Flash. Mold Flash Shall Not Exceed 0.127mm Pre Side. These Dimensions Are Measured At The Outermost Extreme Of The Plastic Body.
5. Thermal Pad Contour Optional Within Dimensions E, H1, D2 & E1.
6. Dimension E2 & H1 Define A Zone Where Stamping And Singulation Irregularities Are Allowed.
7. "*" is reference .

Figure 2 Outline TO-252 Dimensions in mm



8. Appendix

CoolSemi Webpage: www.coolsemi.com.