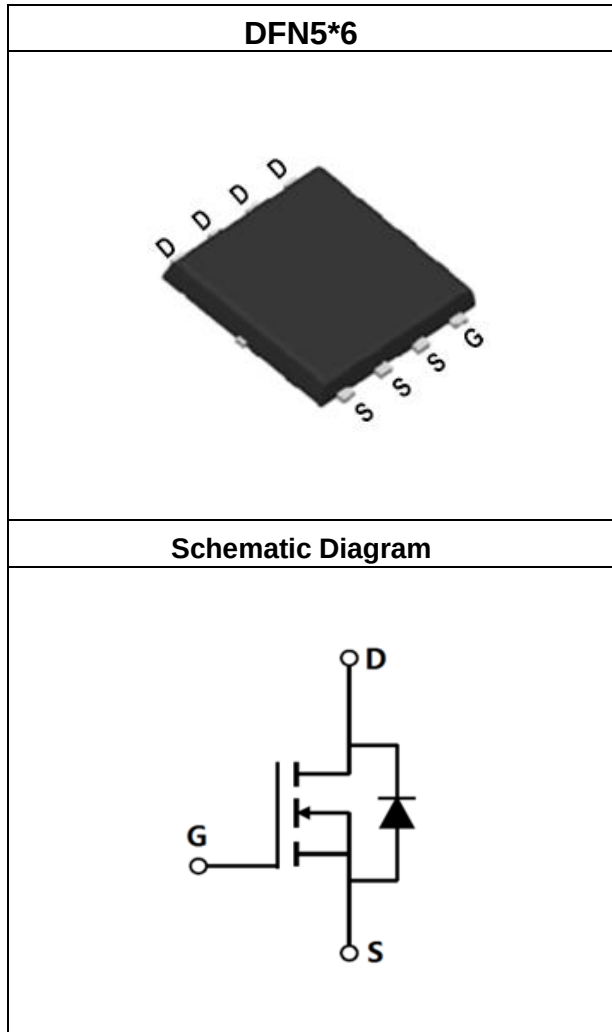


N-channel 40V, 2.7mΩ typ., 120A
 SGT MOSFET S2 in DFN5*6

Datasheet - production data

1. Descriptions



Key Performance Parameters

Parameters	Value	Unit
BV_{DSS}	40	V
$R_{DS(on),max}$	3.2	mΩ
$Q_{g,typ}$	42	nC
$I_{D,pulse}$	400	A
E_{AS}	650	mJ

Features

- Extremely low losses due to very low FOM $R_{dson} \cdot Q_g$.
- High-speed switching.
- Qualified for Automotive applications according to AEC-Q101.
- 100% UIS Tested.

Applications

High-Efficiency DC-DC Converters, Switching Voltage Regulators and Motor Drivers.

Type/Ordering Code	Package	Marking	Related Links
CSLS030N04S2HA	DFN5*6	030N04S2HA	see Appendix A

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2. Maximum Ratings

at $T_j = 25^\circ\text{C}$, unless otherwise specified

Table 1. Absolute Maximum Ratings

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
V_{DS}	Drain-source voltage ¹⁾	-	-	40	V	$V_{GS}=0V$, $I_D=250\mu A$
I_D	Continuous drain current (Silicon Limited)	-	-	120	A	$T_C=25^\circ\text{C}$
		-	-	85	A	$T_C=100^\circ\text{C}$
$I_{D,pulse}$	Pulsed drain current	-	-	400	A	$T_C=25^\circ\text{C}$
E_{AS}	Avalanche energy, single pulse ²⁾	-	-	650	mJ	$I_D=51A$; $V_{DD}=50V$
I_{AS}	Avalanche current	-	-	51	A	-
V_{GS}	Gate source voltage	-20	-	20	V	static; AC ($f>1\text{ Hz}$)
P_{tot}	Power dissipation	-	-	75	W	$T_C=25^\circ\text{C}$
T_j, T_{stg}	Operating and storage temperature	-55	-	175	$^\circ\text{C}$	-
I_S	Continuous diode forward current	-	-	120	A	$T_C=25^\circ\text{C}$
$I_{S,pulse}$	Diode pulse current ²⁾	-	-	400	A	$T_C=25^\circ\text{C}$

1) Limited by T_j max. Maximum duty cycle $D=0.75$.

2) $V_{DD}=50V$, $L=0.5mH$, $R_G=25\Omega$, Starting $T_j=25^\circ\text{C}$.

3. Thermal Characteristics

Table 2. Thermal Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
R_{thJC}	Thermal resistance, junction - case	-	-	2	°C/W	$T_C = 25^{\circ}\text{C}$
R_{thJA}	Thermal resistance, junction - ambient	-	-	65	°C/W	$T_C = 25^{\circ}\text{C}$
T_{sold}	Soldering temperature, wavesoldering only allowed at leads	-	-	260	°C	Lead Temperature (Soldering, 10 sec)

4. Electrical Characteristics

at $T_j = 25^\circ\text{C}$, unless otherwise specified

Table 3. Static Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
$V_{(BR)DSS}$	Drain-source breakdown voltage	40	-	-	V	$V_{GS}=0V, I_D=250\mu A$
$V_{(GS)th}$	Gate threshold voltage	2	2.5	4	V	$V_{DS}=V_{GS}, I_D=250\mu A$
I_{DSS}	Zero gate voltage drain current	-	-	1	μA	$V_{DS}=40V, V_{GS}=0V, T_j=25^\circ C$
I_{GSS}	Gate-source leakage current	-	-	± 100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
$R_{DS(on)}$	Drain-source on-state resistance	-	2.7	3.2	m Ω	$V_{GS}=10V, I_D=20A, T_j=25^\circ C$
R_G	Gate resistance	-	2.6	-	Ω	$V_{DD}=0V, V_{GS}=0V, F=1MHz$
g_{fs}	Transconductance		130		S	$V_{DS}=5V, I_D=20A$

Table 4. Dynamic Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
C_{iss}	Input capacitance	-	4200	-	pF	$V_{GS}=0V, V_{DS}=25V, f=250kHz$
C_{oss}	Output capacitance	-	1080	-	pF	$V_{GS}=0V, V_{DS}=25V, f=250kHz$
C_{rss}	Reverse transfer capacitance	-	41	-	pF	$V_{GS}=0V, V_{DS}=25V, f=250kHz$
$t_{d(on)}$	Turn-on delay time	-	13.5	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=100A, R_G=3\Omega$
t_r	Rise time	-	96	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=100A, R_G=3\Omega$
$t_{d(off)}$	Turn-off delay time	-	40	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=100A, R_G=3\Omega$
t_f	Fall time	-	115	-	ns	$V_{DD}=30V, V_{GS}=10V, I_D=100A, R_G=3\Omega$

Table 5. Gate Charge Characteristics

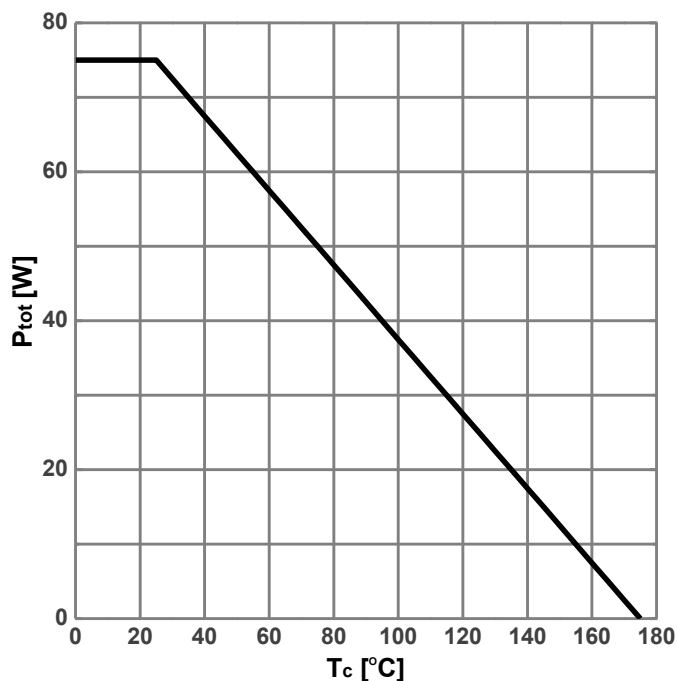
Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
Q_{gs}	Gate to source charge	-	10	-	nC	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$
Q_{gd}	Gate to drain charge	-	12	-	nC	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$
Q_g	Gate charge total	-	42	-	nC	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$
$V_{plateau}$	Gate plateau voltage	-	2.8	-	V	$V_{DD}=30V, I_D=20A, V_{GS}=0 \text{ to } 10V$

Table 6. Reverse Diode Characteristics

Symbol	Parameter	Values			Unit	Test Condition
		Min.	Typ.	Max.		
V_{SD}	Diode forward voltage	-	0.8	-	V	$V_{GS}=0V, I_F=20A, T_j=25^\circ C$
t_{rr}	Reverse recovery time	-	35	-	ns	$V_R=30V, I_F=60A, di_F/dt=100A/\mu s$
Q_{rr}	Reverse recovery charge	-	30	-	nC	$V_R=30V, I_F=60A, di_F/dt=100A/\mu s$
I_{rrm}	Peak reverse recovery current	-	1.5	-	A	$V_R=30V, I_F=60A, di_F/dt=100A/\mu s$

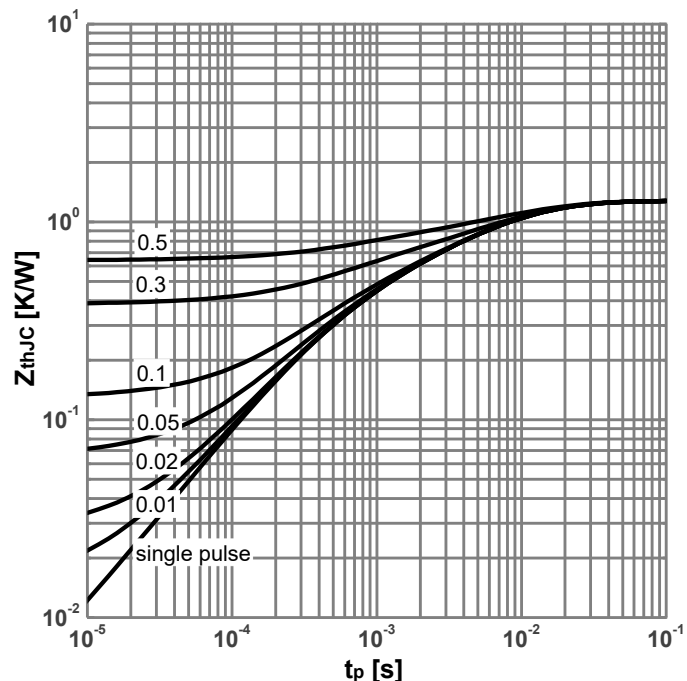
5. Electrical Characteristics Diagrams

Diagram 1: Power dissipation



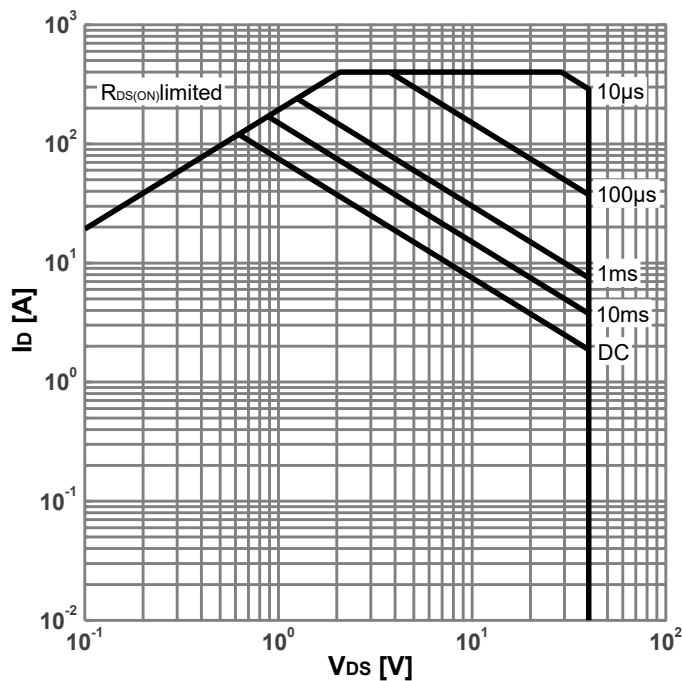
$$P_{tot}=f(T_c)$$

Diagram 2: Max. transient thermal impedance



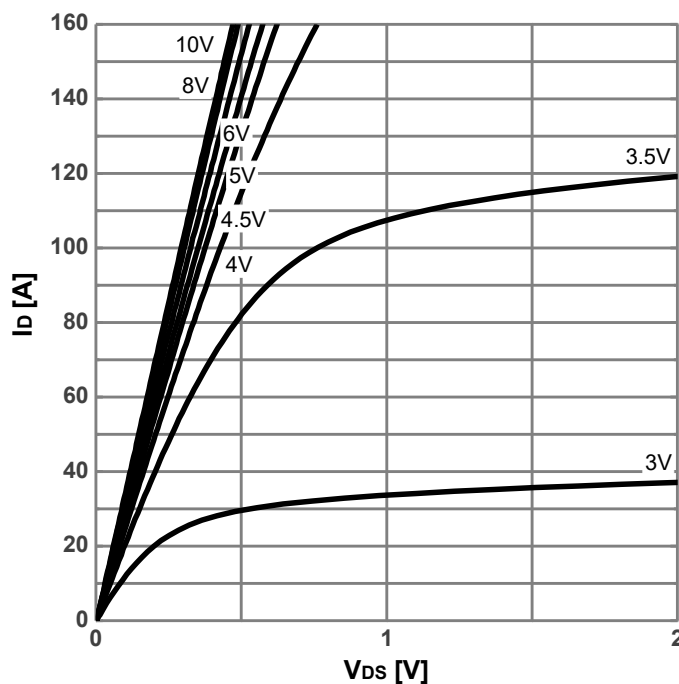
$$Z_{thJC}=f(t_p); \text{ parameter: } D= t_p/T$$

Diagram 3: Safe operating area



$$I_D=f(V_{DS}); T_J=25^{\circ}\text{C}; D=0; \text{ parameter: } t_p$$

Diagram 4: Typ. output characteristics



$$I_D=f(V_{DS}); T_J=25^{\circ}\text{C}; \text{ parameter: } V_{GS}$$

Diagram 5: Typ. output characteristics

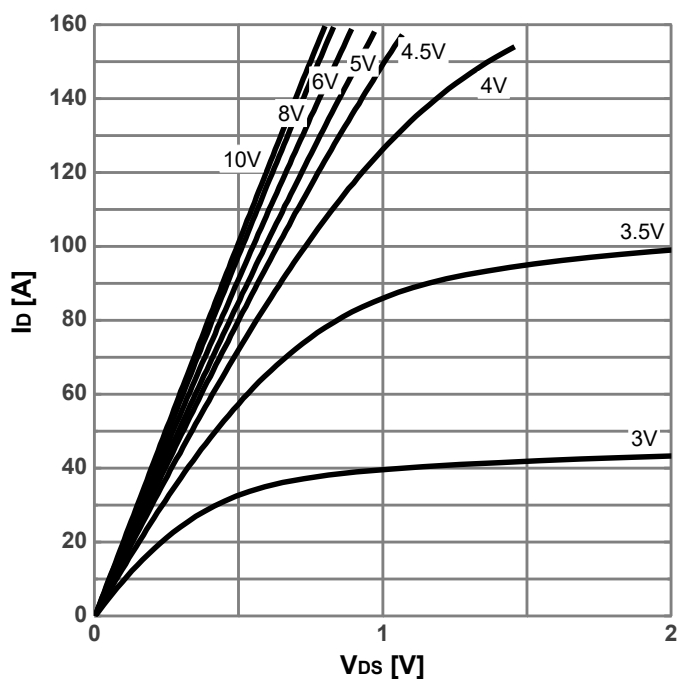

 $I_D = f(V_{DS}); T_J = 125^\circ\text{C}; \text{parameter: } V_{GS}$

Diagram 6: Typ. transfer characteristics

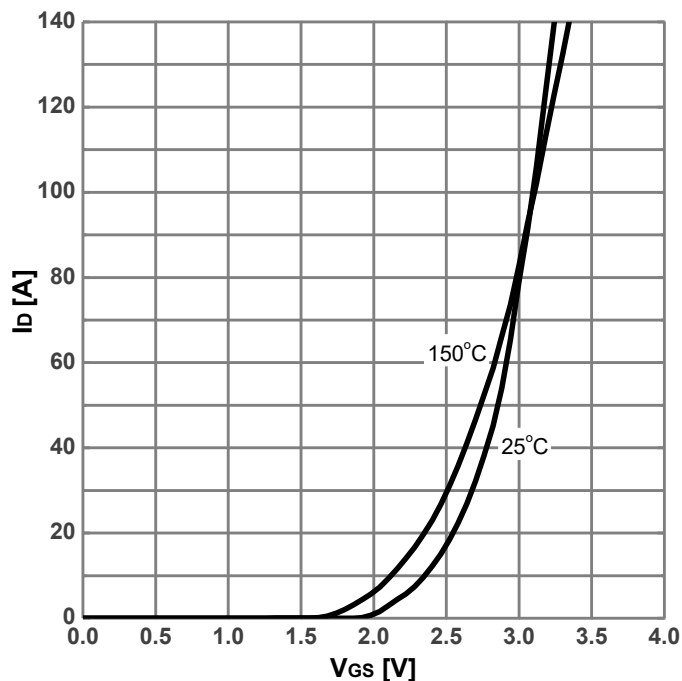

 $I_D = f(V_{GS}); V_{DS} = 20\text{V}; \text{parameter: } T_J$

Diagram 7: Gate threshold voltage vs. Junction temperature

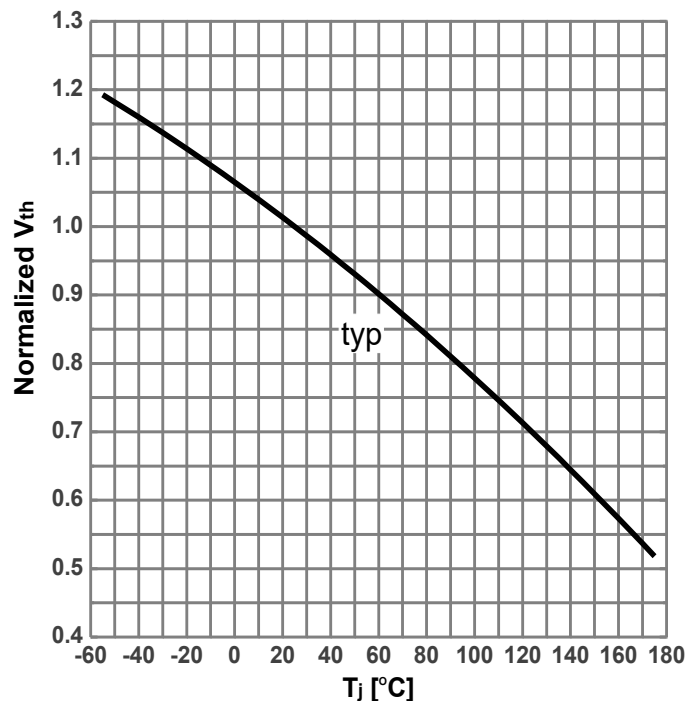

 $V_{th} = f(T_J); I_D = 250\mu\text{A}$

Diagram 8: On-state resistance vs. Drain current

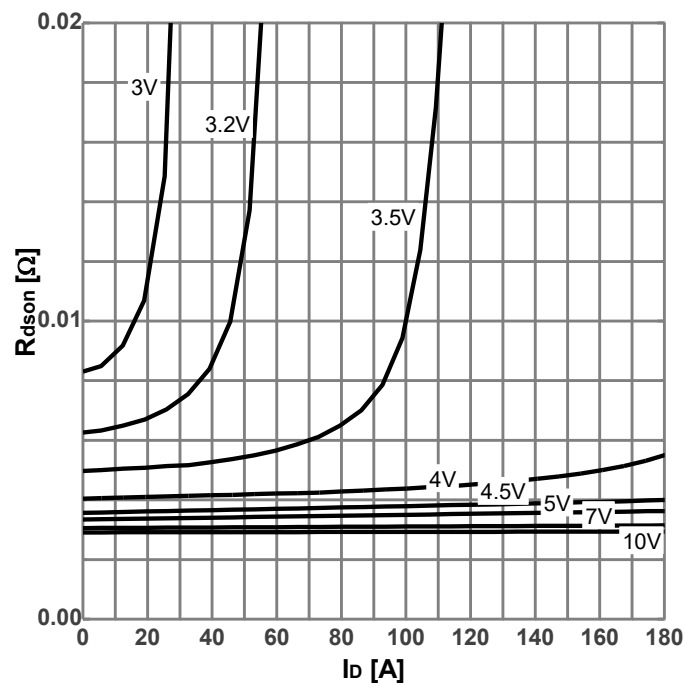
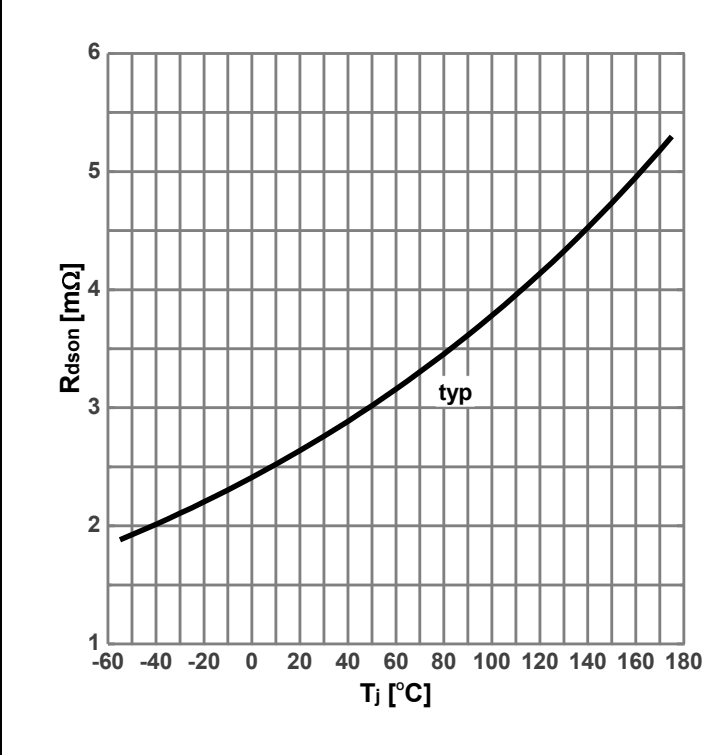
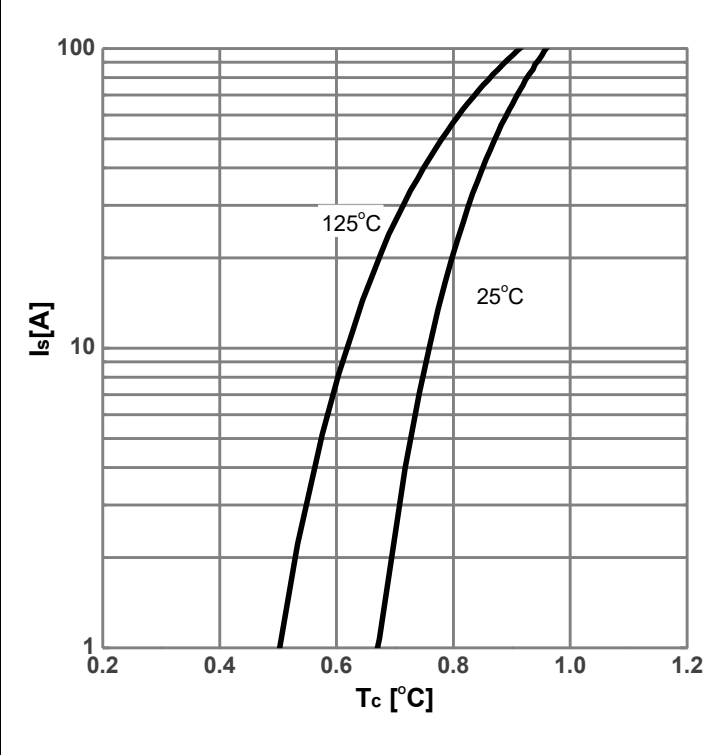

 $R_{DS(on)} = f(I_D); T_J = 25^\circ\text{C}; \text{parameter: } V_{GS}$

Diagram 9: On-state resistance vs. Junction temperature



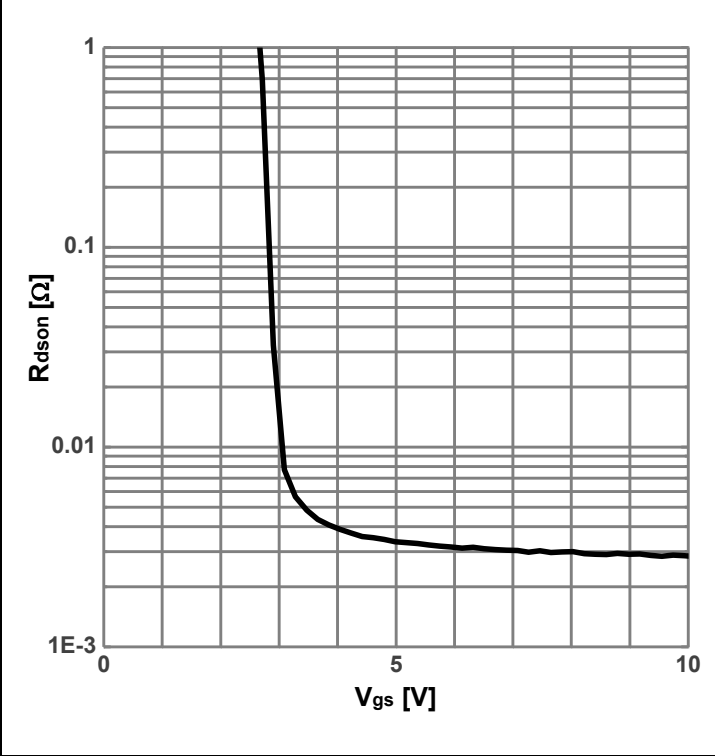
$R_{DS(on)}=f(T_j)$; $I_D=20A$; $V_{GS}=10V$

Diagram 10: Forward characteristics of reverse diode



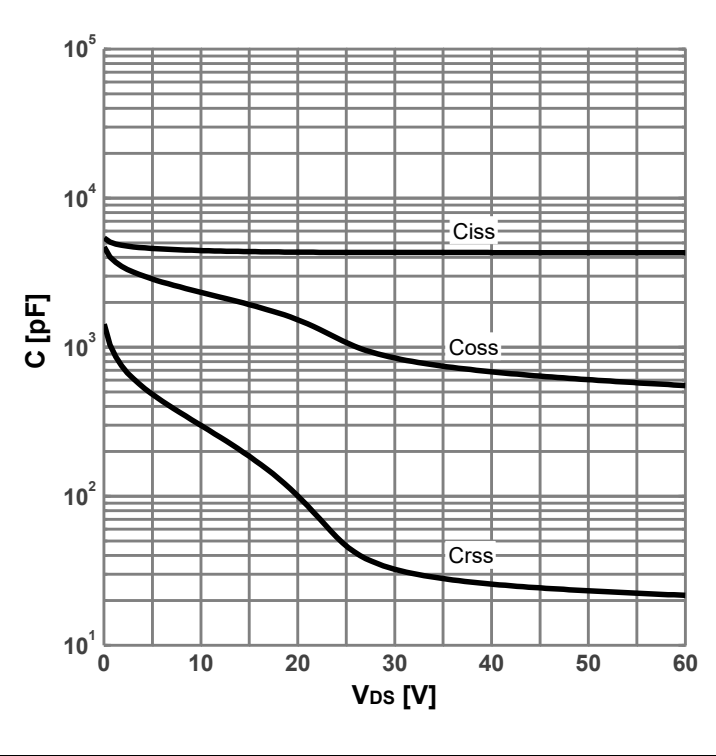
$I_F=f(V_{SD})$; parameter: T_j

Diagram 11: On-state resistance vs. Vgs characteristics



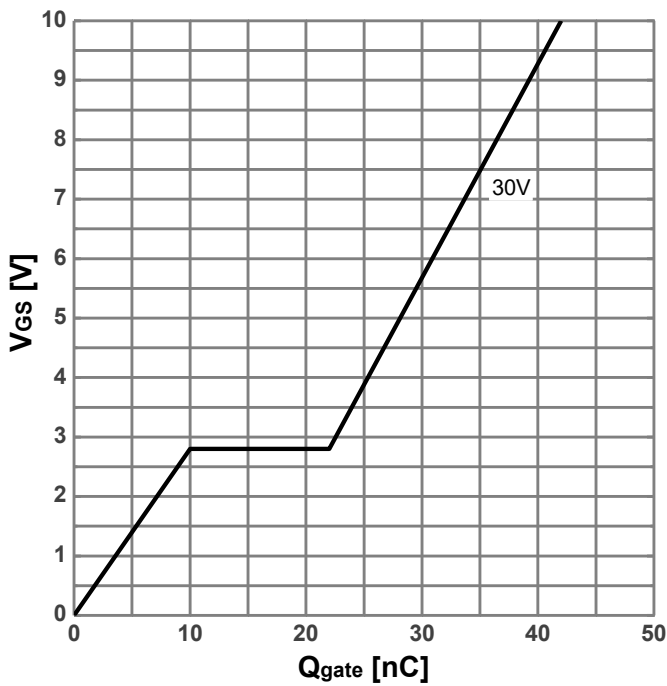
$R_{DS(on)}=f(V_{gs})$; $T_j=25^{\circ}C$; $I_D=20A$

Diagram 12: Typ. capacitances



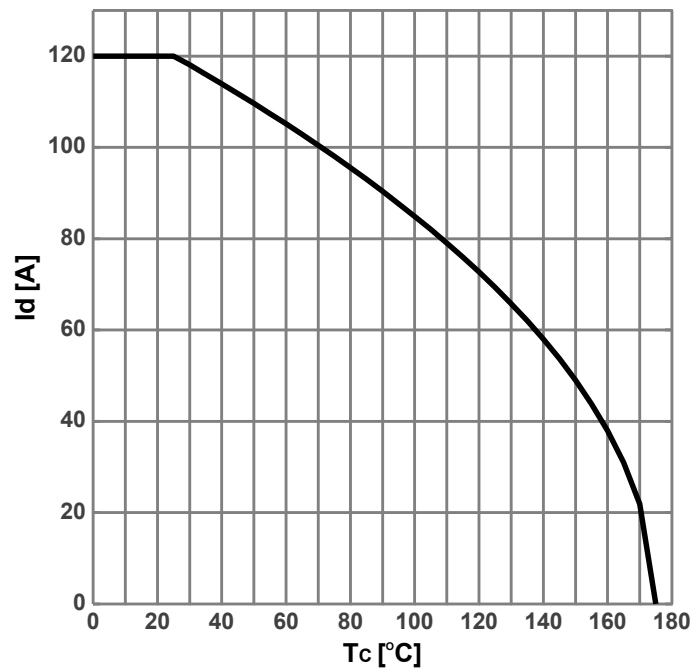
$C=f(V_{DS})$; $V_{GS}=0V$; $f=250kHz$

Diagram 13: Typ. gate charge



$V_{GS}=f(Q_{gate})$; $I_D=20A$ pulsed; $V_{DS}=30V$

Diagram 14: Maximum Drain Current



$I_D=f(T_c)$; $V_{GS}=10V$

6. Test Circuits

Table 7. Diode Characteristics

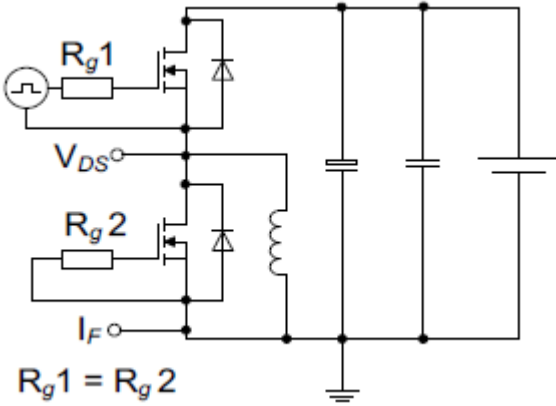
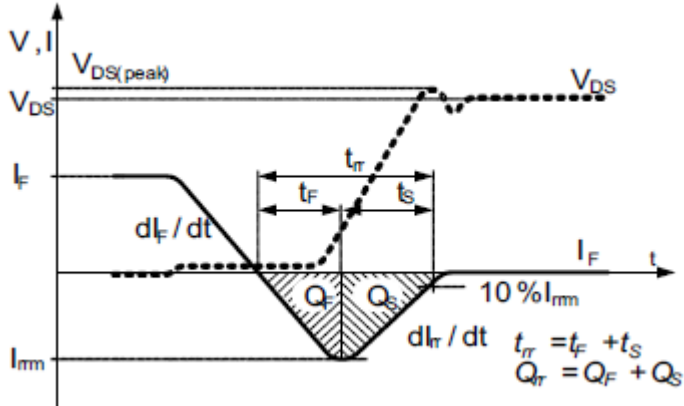
Test circuit for diode characteristics	Diode recovery waveform
	

Table 8. Switching Times

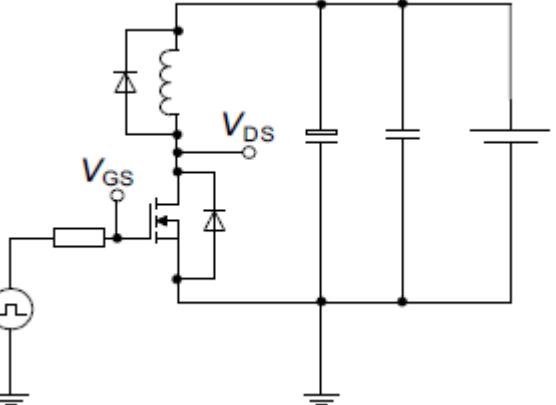
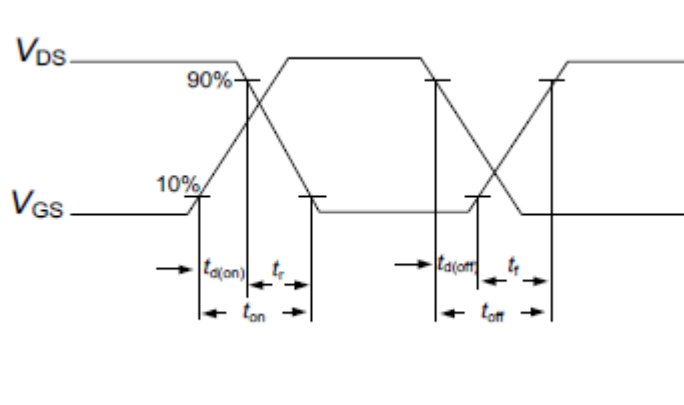
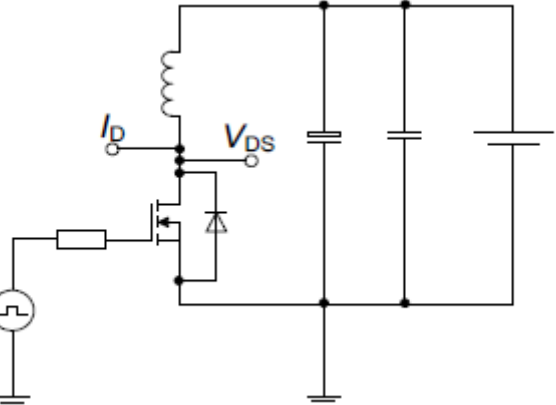
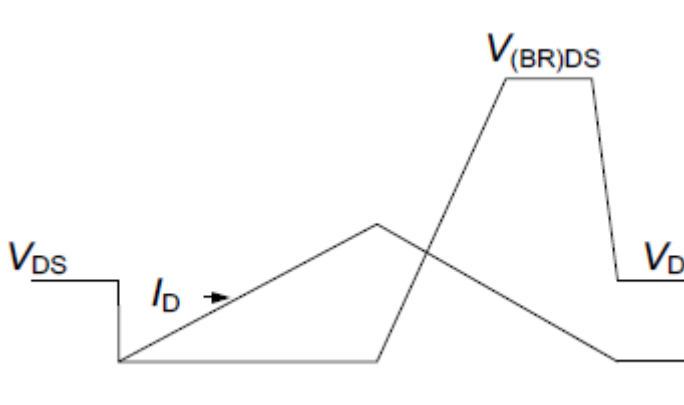
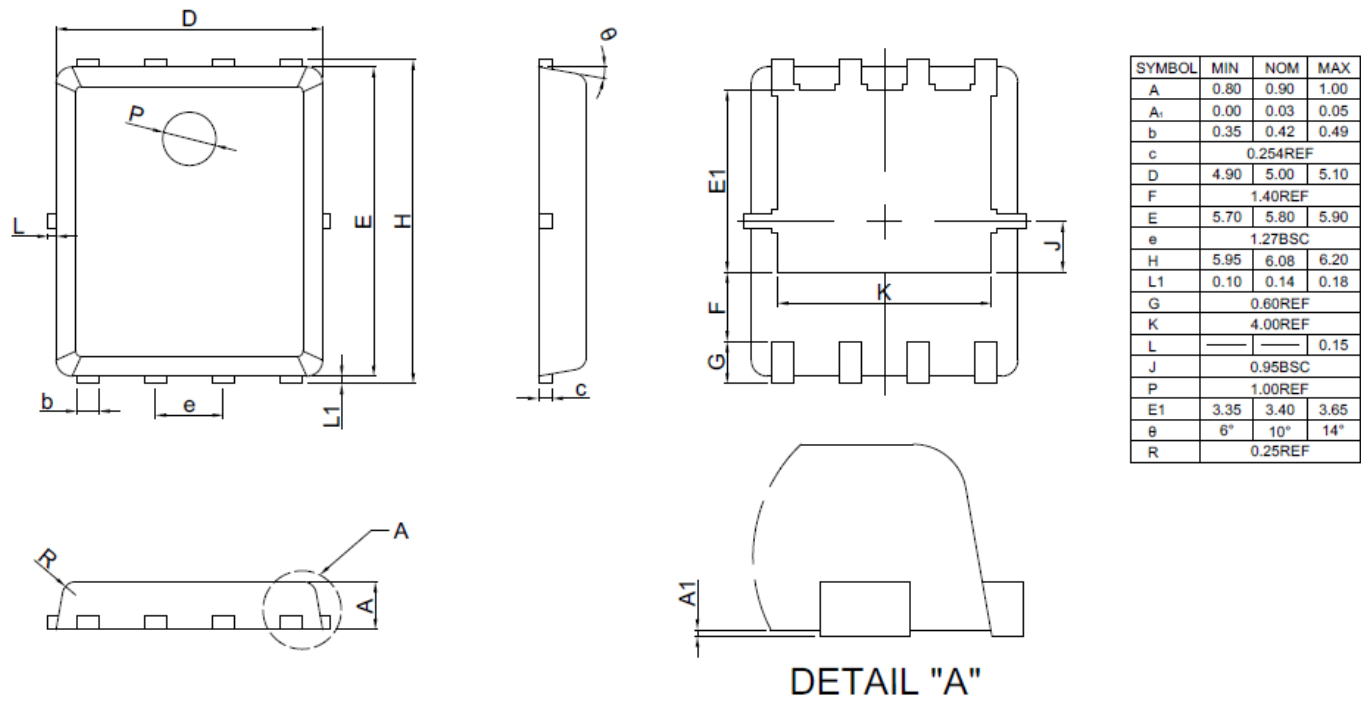
Switching times test circuit for inductive load	Switching times waveform
	

Table 9. Unclamped Inductive Load

Unclamped inductive load test circuit	Unclamped inductive waveform
	

7. Package Outlines

Figure 1 Outline DFN5*6 Dimensions in mm



8. Appendix

CoolSemi Webpage: www.coolsemi.com.